

Figure 1K

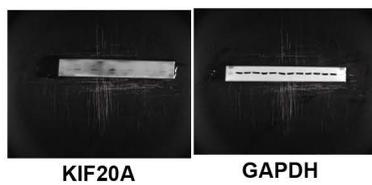


Figure 2C

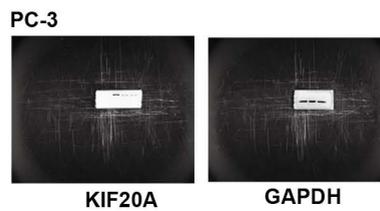
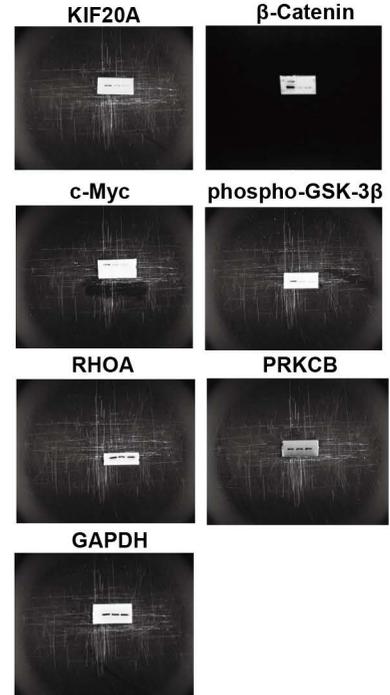
Figure 4D
PC-3

Figure 1L

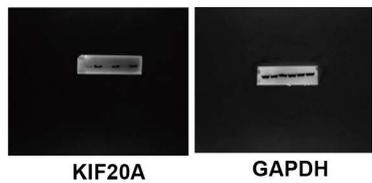


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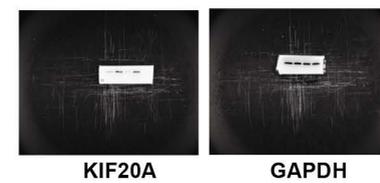
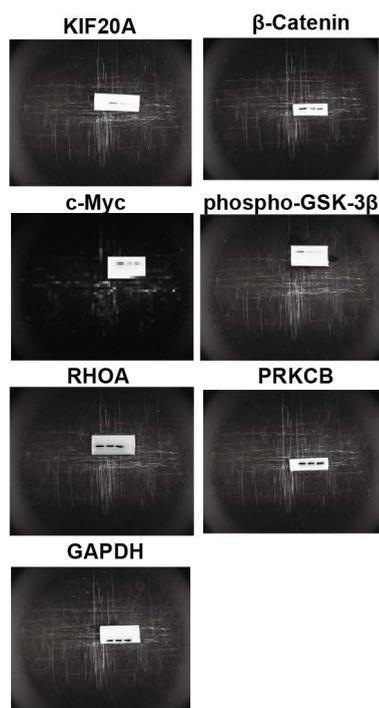
Figure 4D
C4-2

Figure 4E

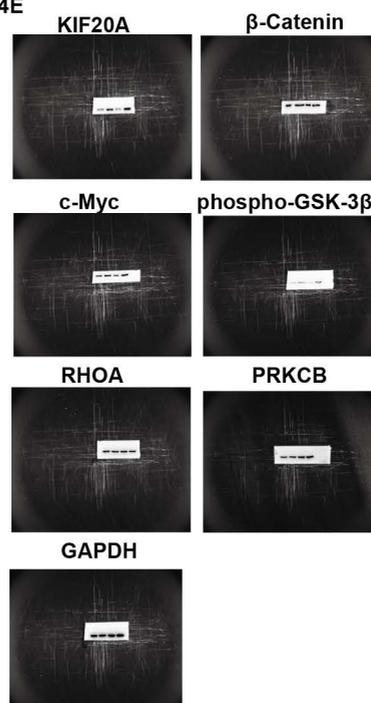


Figure 4F

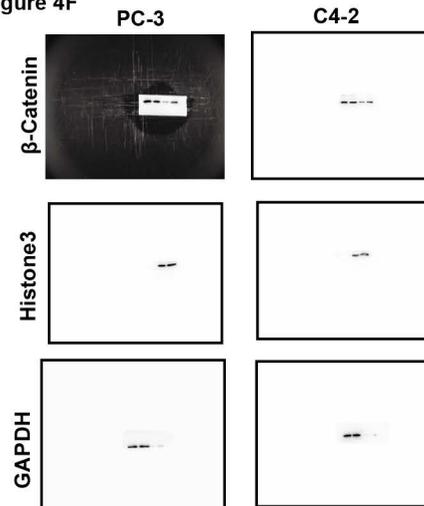
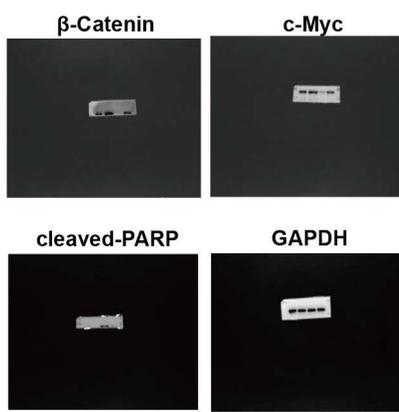
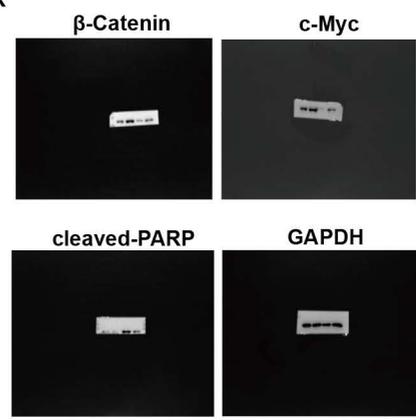
Figure 4J
PC-3Figure 4K
C4-2

Figure 5D

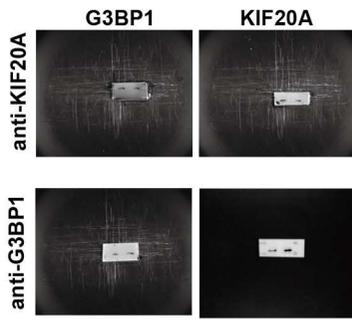


Figure 5E

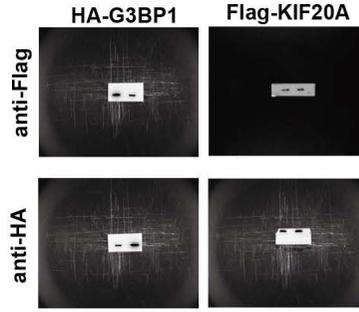


Figure 5G

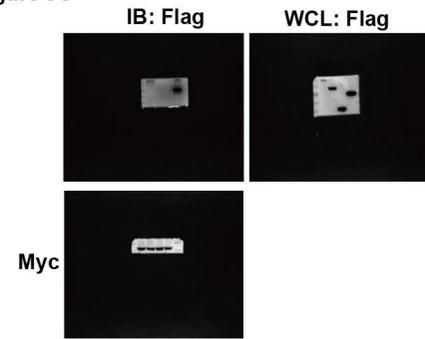


Figure 5H

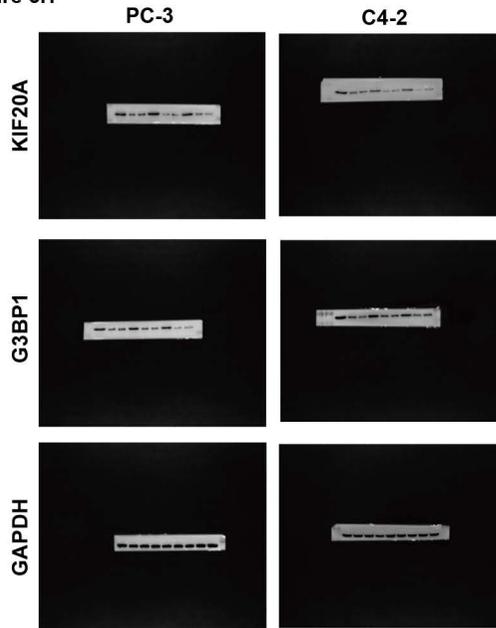


Figure 5J

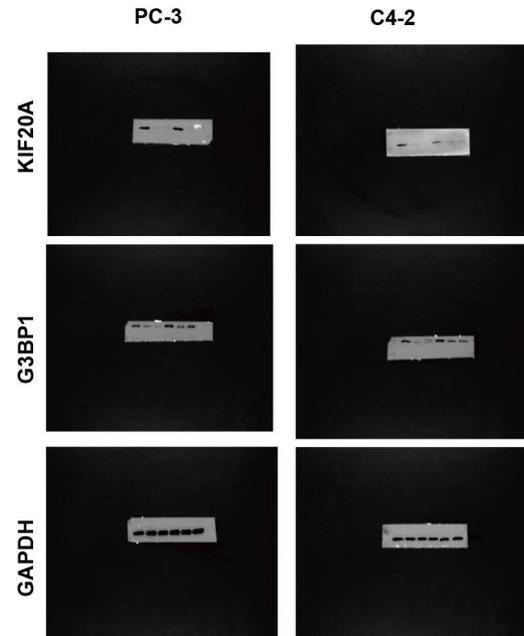


Figure 5L

PC-3



Figure 5N

C4-2

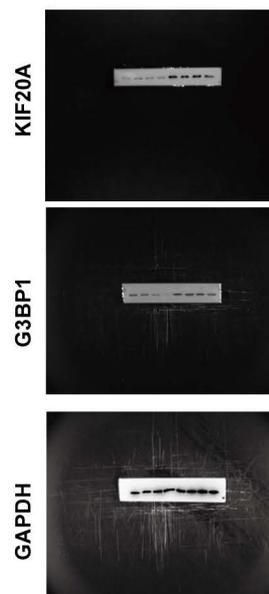


Figure 6A

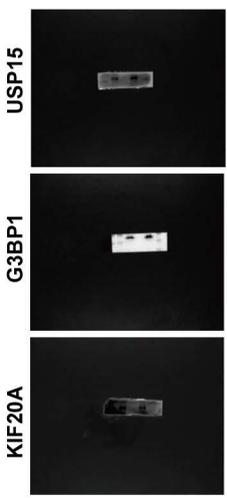


Figure 6B

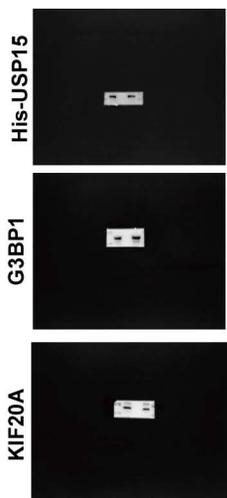


Figure 6C

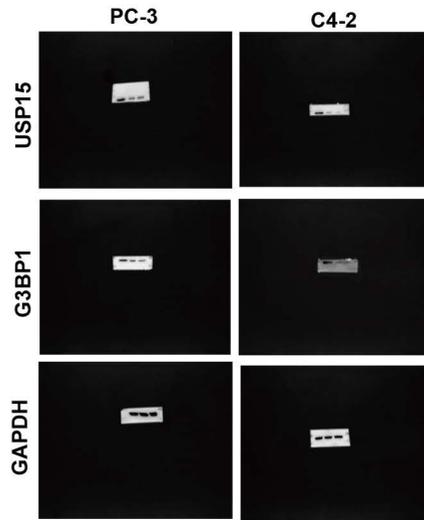


Figure 6E



Figure 6G



Figure 6I



Figure 6J

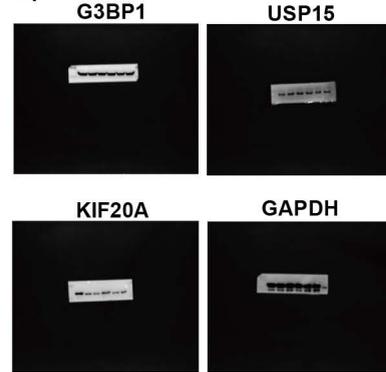
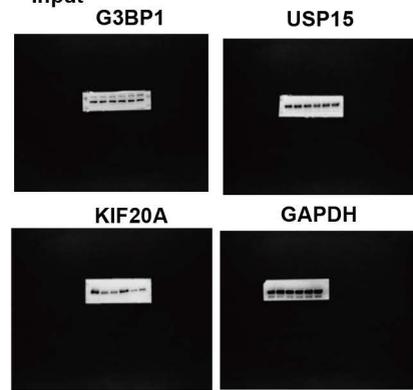
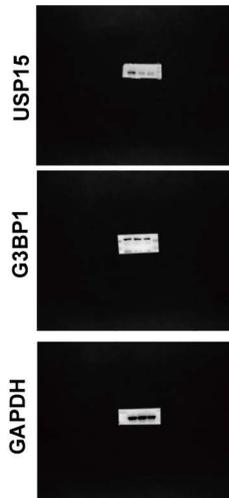
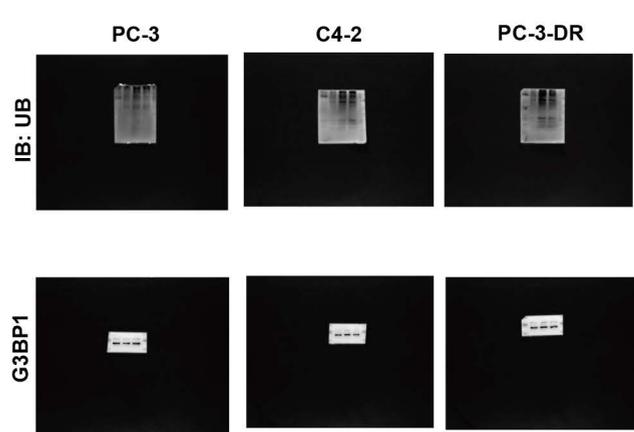
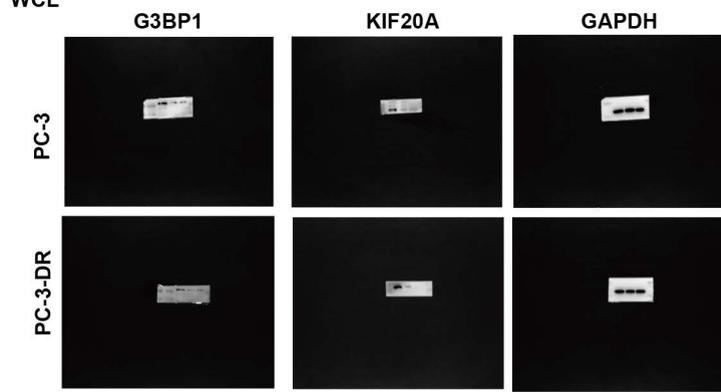
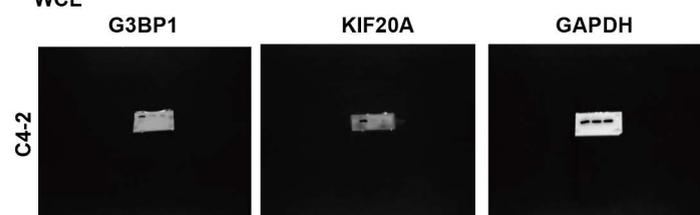
Figure 6K
IP: USP15Figure 6K
InputFigure 6L
IP: G3BP1Figure 6L
Input

Figure 6M



Figure 6N

Figure 6O
IP: G3BP1Figure 6O
WCLFigure 6O
WCL

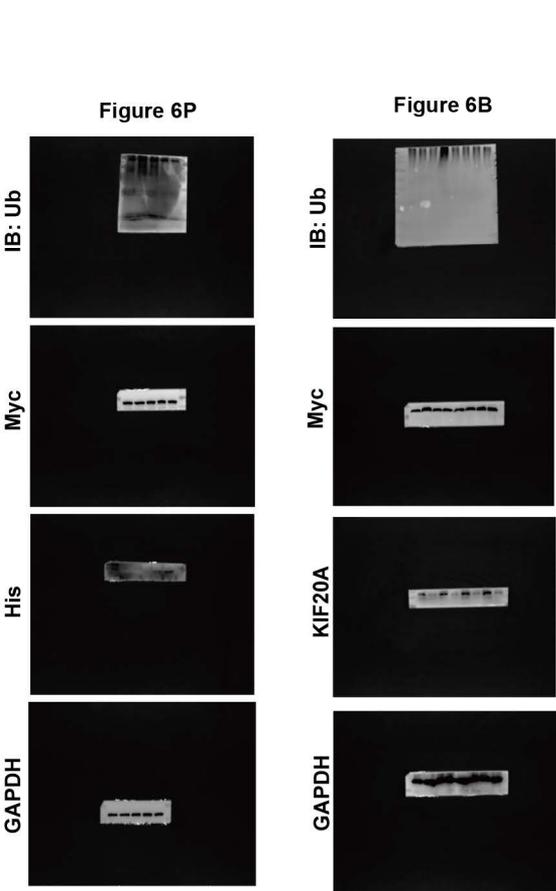
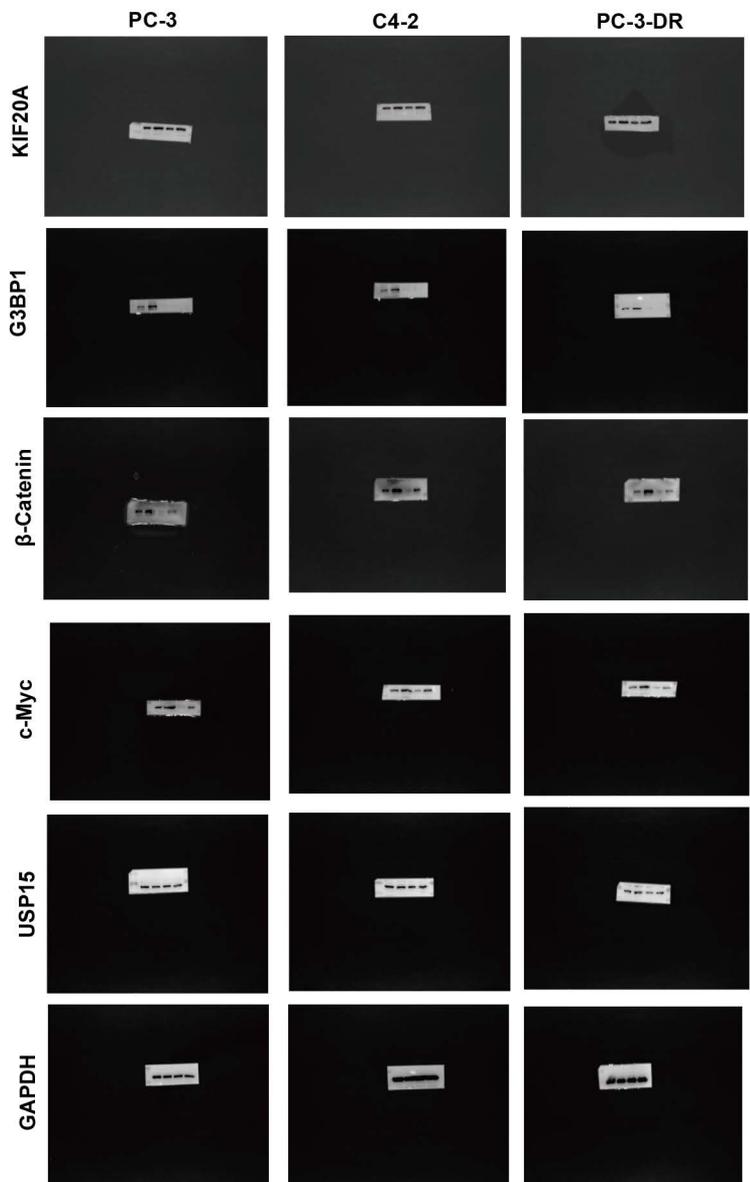
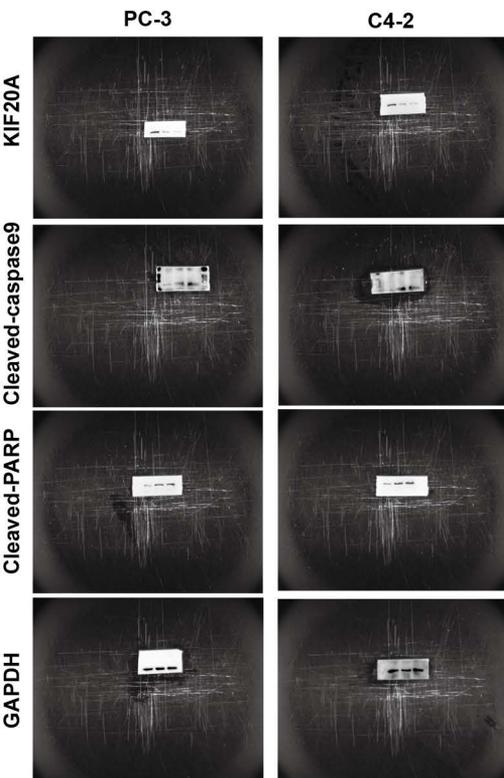


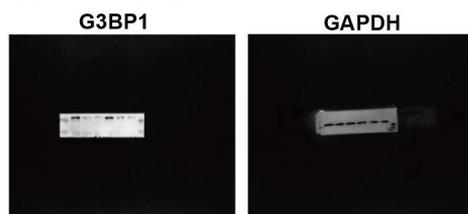
Figure 7I



Supplementary figure 2I



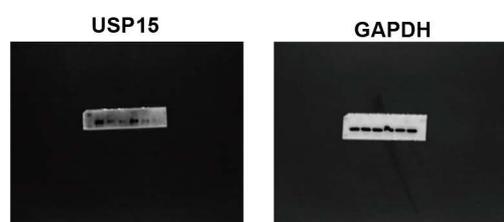
Supplementary figure 5C



Supplementary figure 5H



Supplementary figure 6D



Supplementary figure 6F

