

Fig. 3A

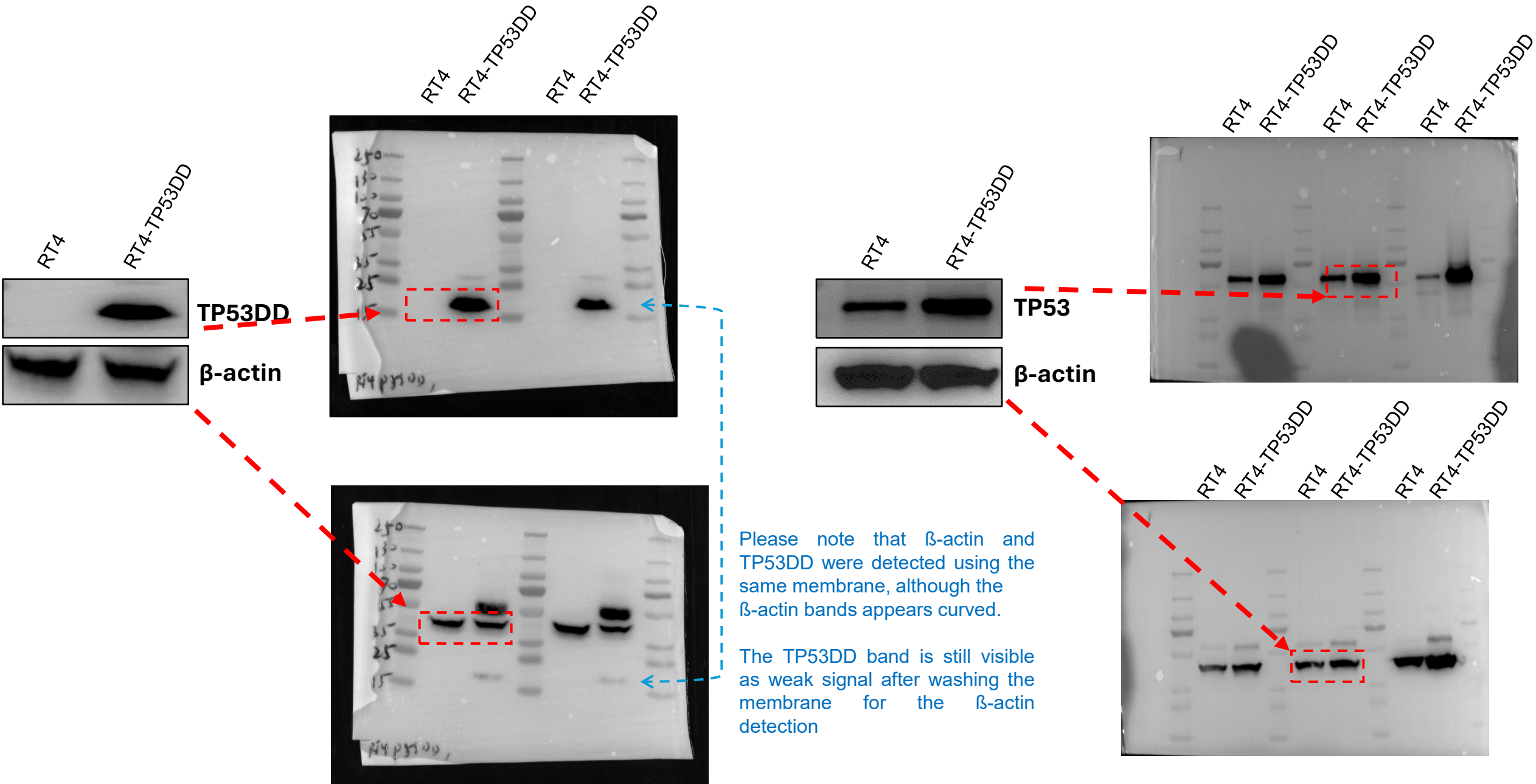


Fig. 5D

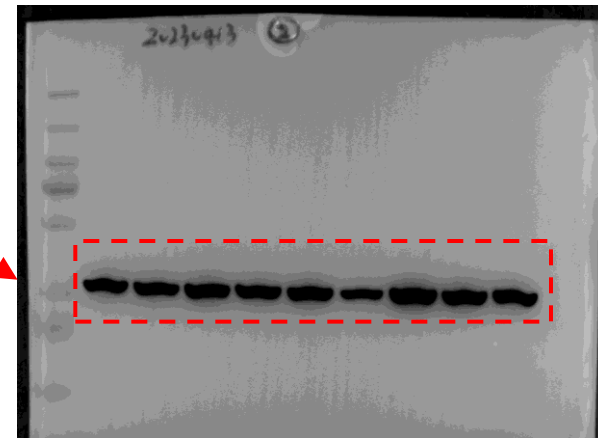
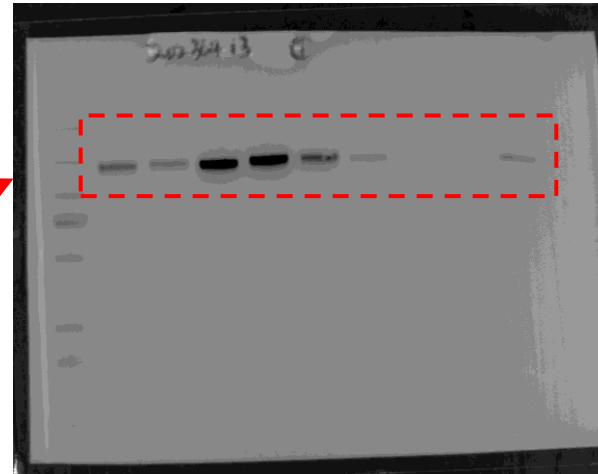
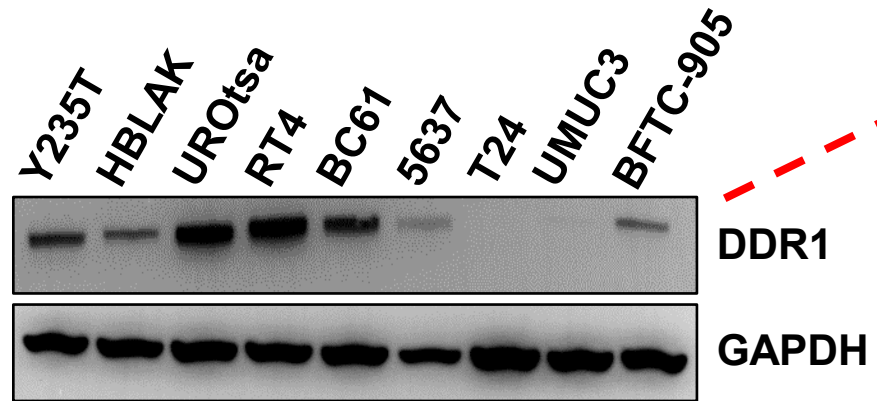
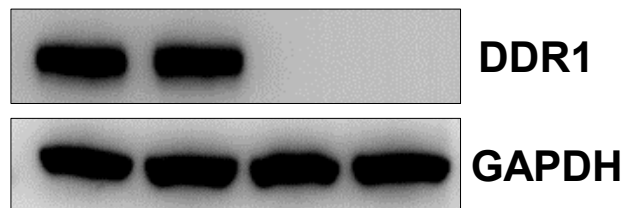


Fig. 6

A

RT4-TP53DD-gGFP#1
RT4-TP53DD-gGFP#2
RT4-TP53DD-gDDR1#1
RT4-TP53DD-gDDR1#2

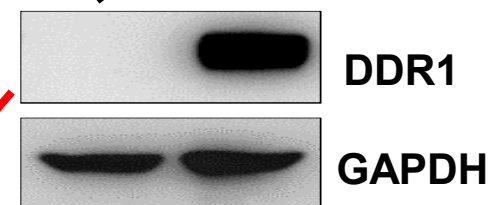


DDR1

GAPDH

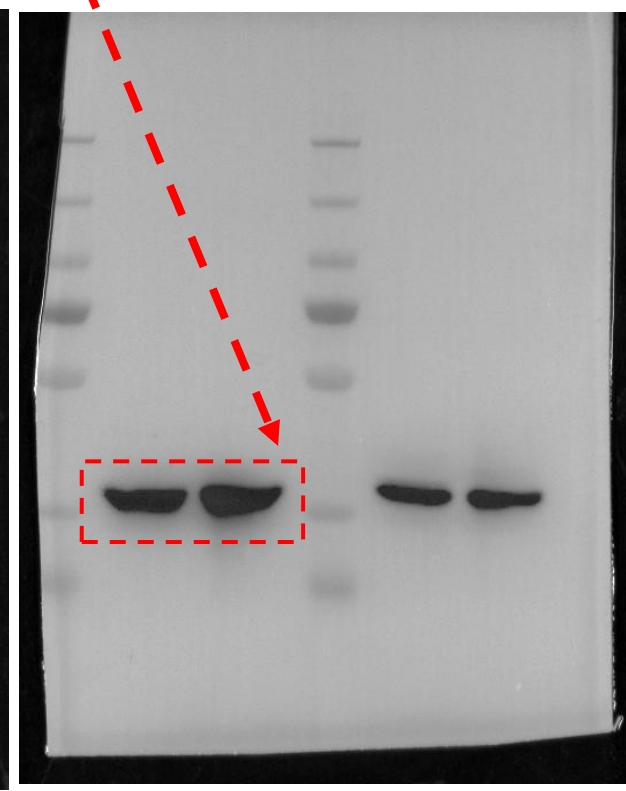
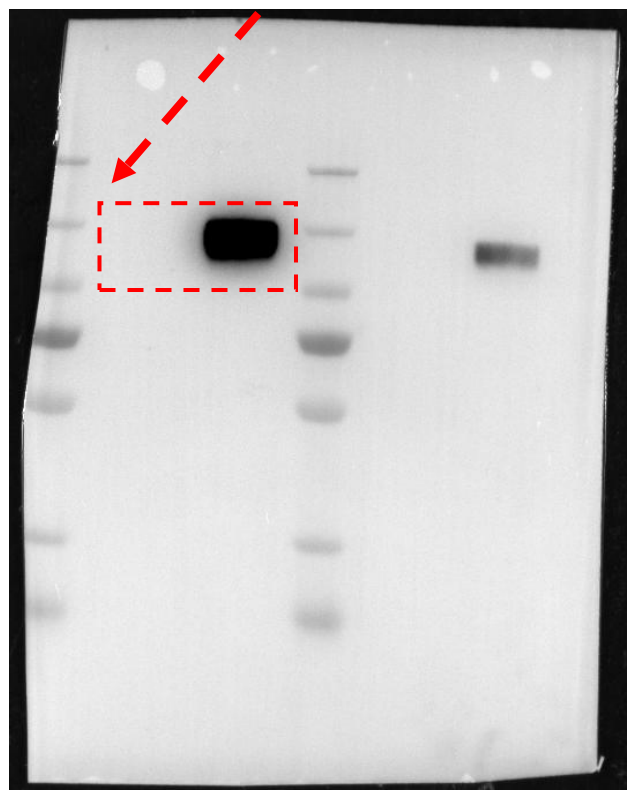
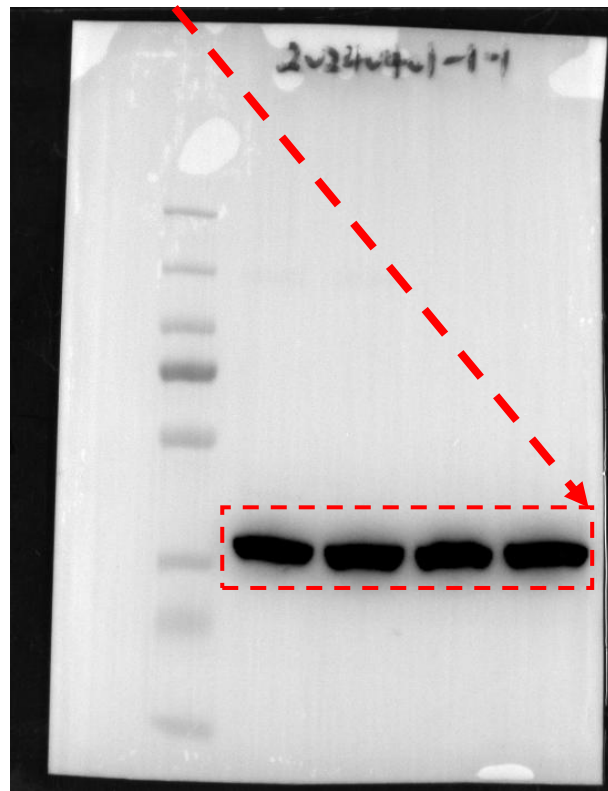
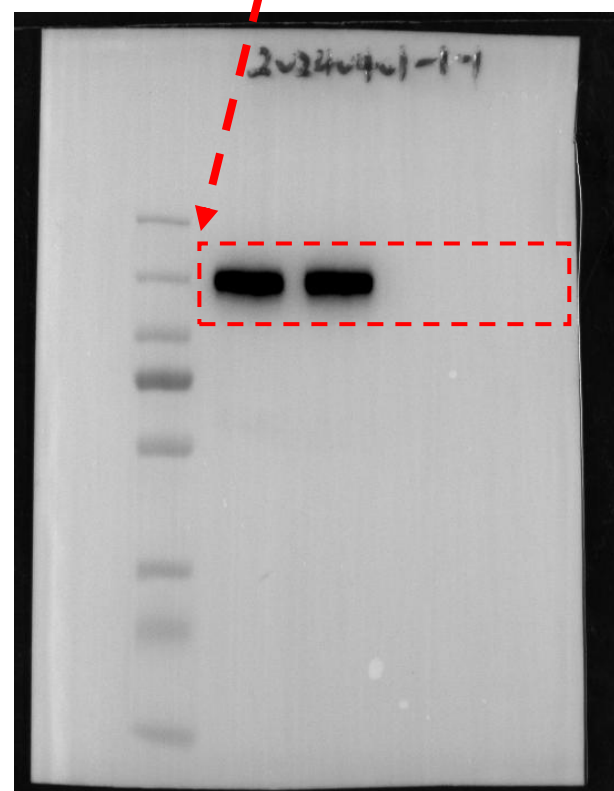
B

T24-EV
T24-DDR1



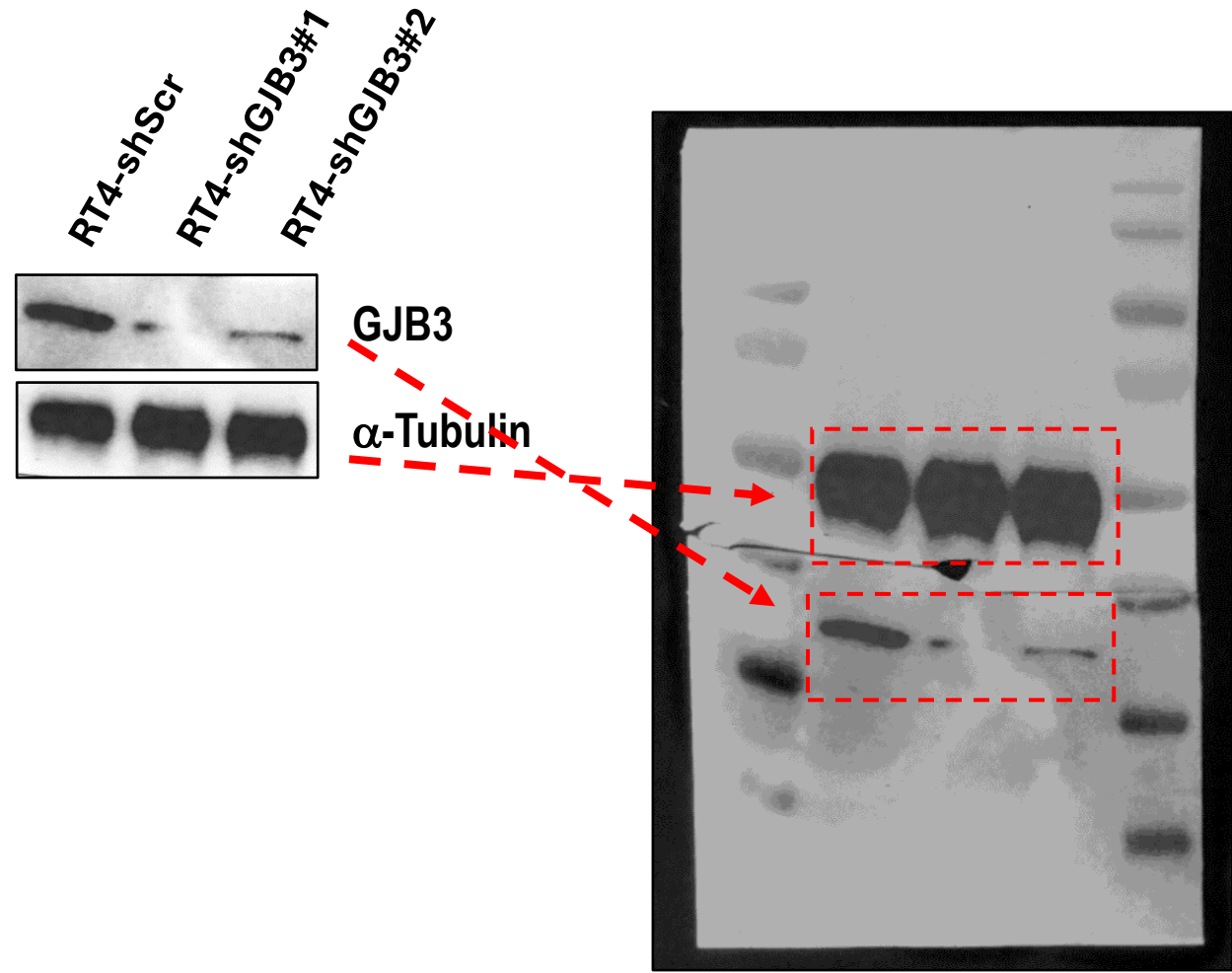
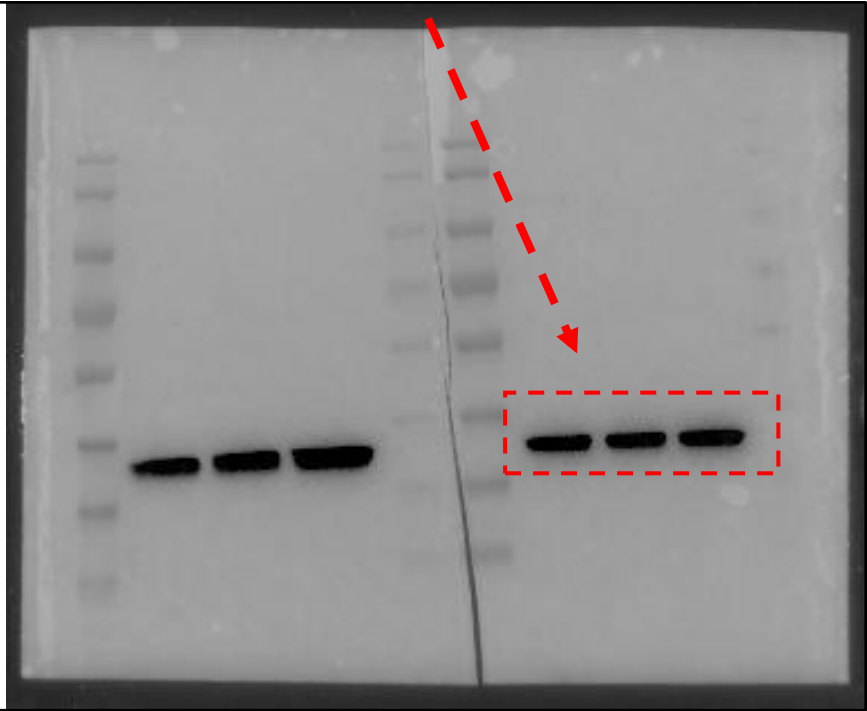
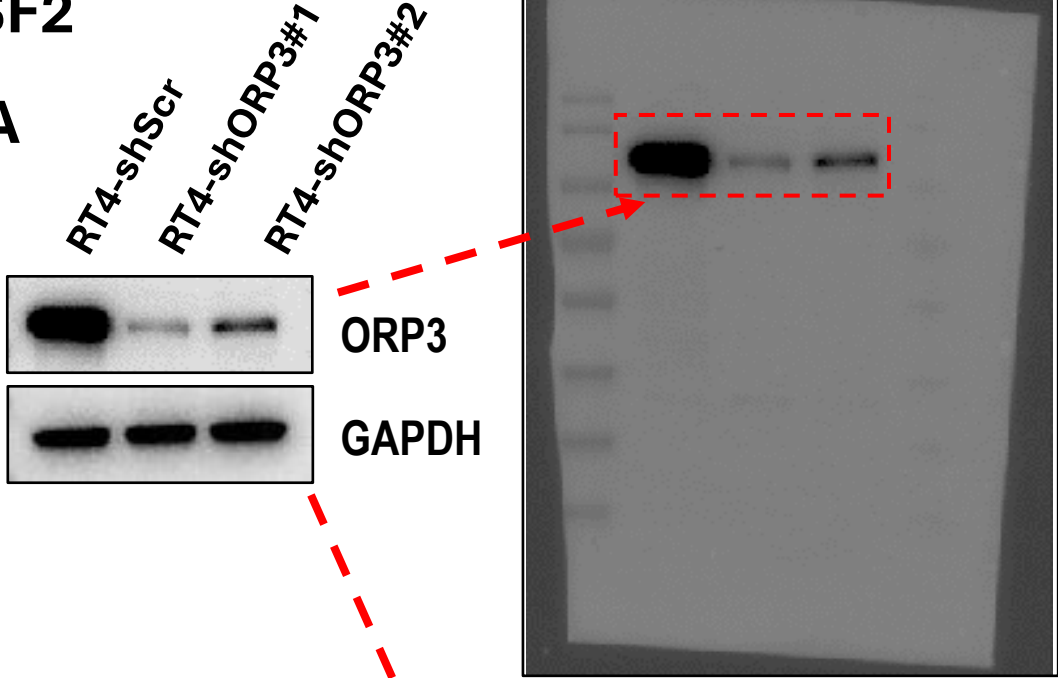
DDR1

GAPDH



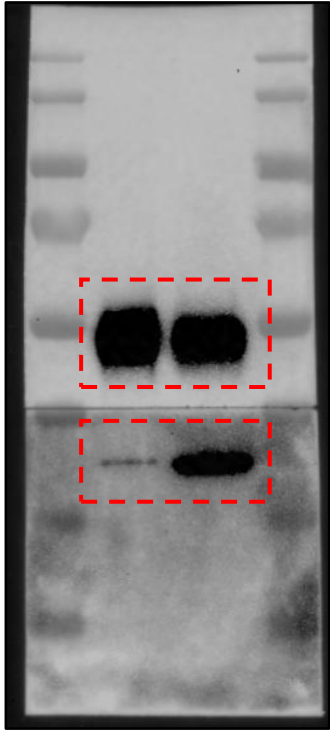
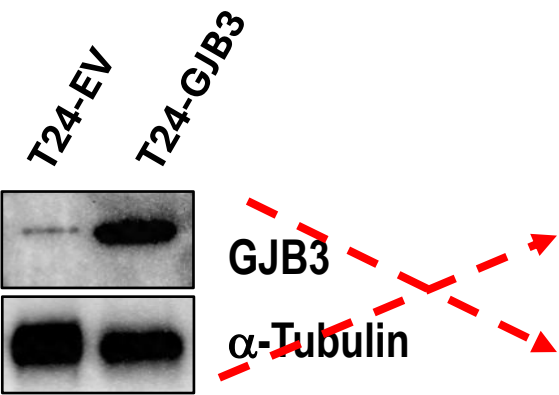
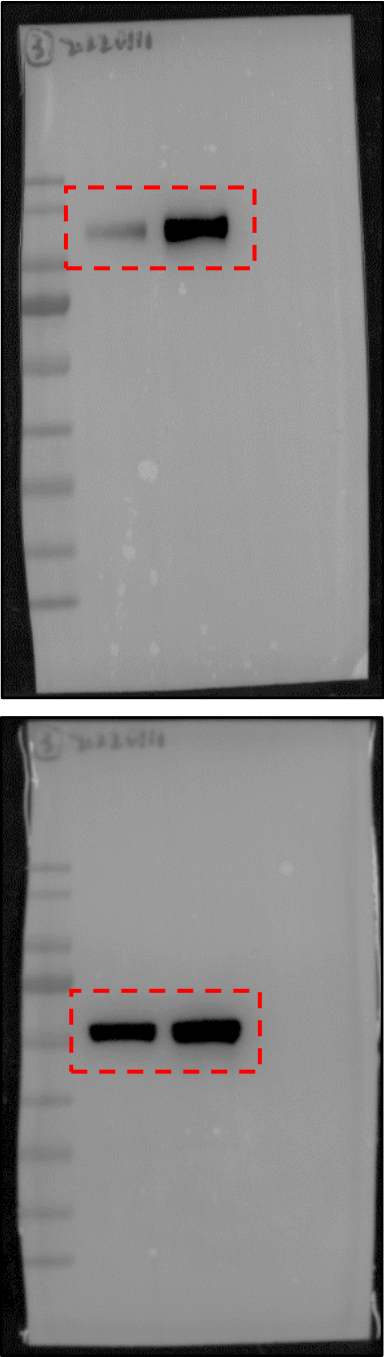
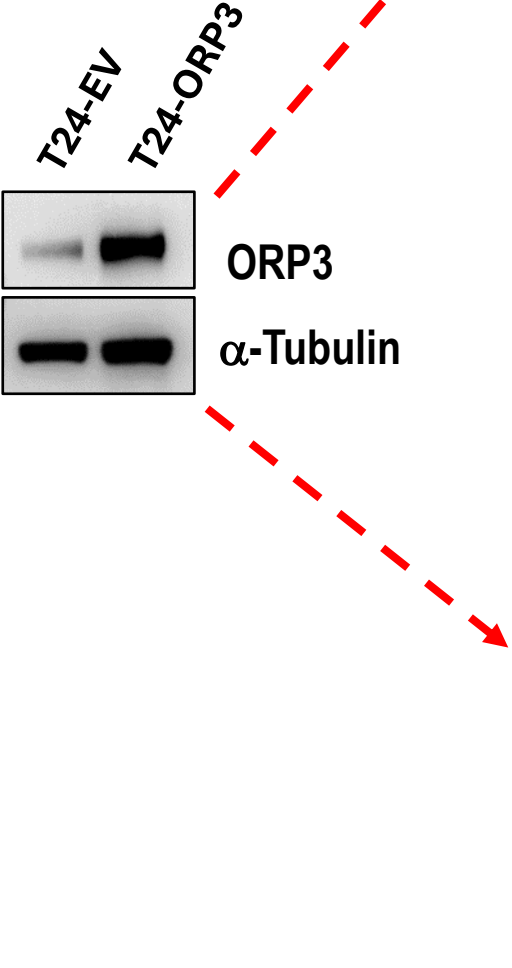
SF2

A



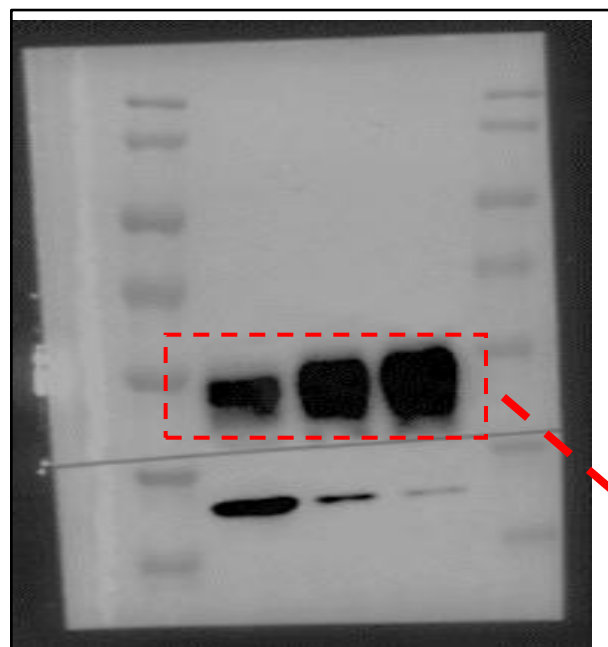
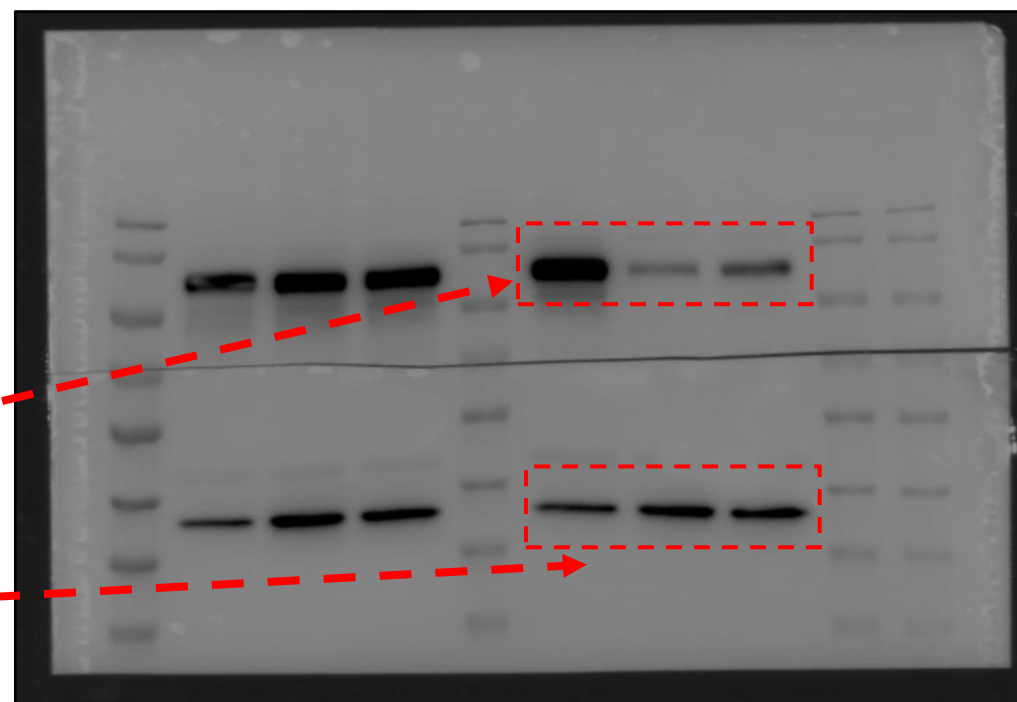
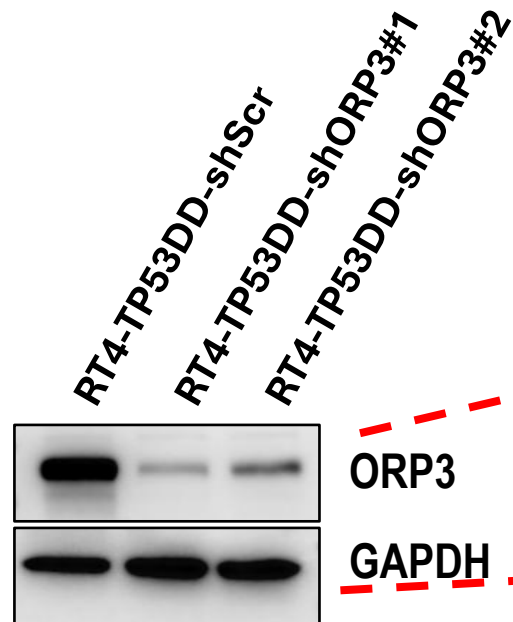
SF2

B



SF3

A



B

