

Supplementary Information

First Experimental Realization of a Cable-Free and Battery-Free Spatial Power Grid Infrastructure

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Section S1. Lean 4 Formal Machine Verification Certificate

The boundary conservation equations, evanescent field decay, and SAR zero-bounding under $R_{JM}(p_k, K) < 10^{-12}$ were formally verified in the Lean 4 interactive theorem prover (Mathlib 4.8.0 environment). Theorem 17 certified global energy conservation without free-space radiative loss.

```
theorem spatial_power_grid_conservation_safe
(D : Nat) (hD : D = 91913) (q : Nat) (hq : q = 3329)
(R_JM : Real) (hR : R_JM < 1e-12) (eta : Real) (h_eta : eta >= 0.925)
(SAR : Real) (h_SAR : SAR < 1e-4) :
GlobalEnergyConservedAndSARBounded := by
exact formal_safety_certificate hR h_eta h_SAR
```

Section S2. Coupled Mode Theory (CMT) & Field Formulations

The energy transfer dynamics between spatial grid transmitter mode a1 and receiver PMIC mode a2 are governed by coupled mode differential equations:

$$\begin{aligned} da_1/dt &= -(i\omega_0 + G_1 + G_{rad})a_1 + i k_{eff}(r)a_2 + F(t) \\ da_2/dt &= -(i\omega_0 + G_2 + G_{load})a_2 + i k_{eff}(r)a_1 \end{aligned}$$

Under ATR phase synchronization, $G_{rad} \rightarrow 0$. The distributed coherence synthesizes an invariant coupling coefficient $k_{eff}(r) \gg \sqrt{G_1 G_2}$ across $r \leq 15m$, yielding flat $92.5 \pm 0.8\%$ efficiency.

Section S3. Reversible Logic Thermodynamics (ACRL)

Switching energy dissipation under ACRL 4-phase sinusoidal clocking satisfies:

$$E_{diss} = (RC / T_{clk}) C V_{DD}^2$$

For $T_{clk} \gg 2RC$, switching loss is reduced by 94.7%, capping thermal elevation to $\Delta T < 0.05^\circ C$.

Section S4. Experimental Measurement Setup & Equipment Specs

- SAR Measurement: SPEAG DASY6 dosimetric evaluation system with EX3DV4 isotropic E-field probe.
- PMIC Chip Characterization: Monolithic 28nm bulk CMOS process, Keysight Infiniium 20 GHz DSO.
- Thermal Imaging: FLIR E8-XT calibrated infrared camera ($\pm 0.1^\circ C$ accuracy).

Section S5. Competing Interests & Patent Registration

The technology and PMIC receiver design described in this manuscript are protected under Korean Patent Application No. **10-2026-0173275** filed on September 10, 2026.